

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10009530	FILING DATE 01/22/2002	CLASS 216-	SUBCLASS 24	GAU 1740	EXAMINER [Signature]
**APPLICANTS: Martin Trevor; Balmer Richard; Ayling Stephen; Maclean Jessica; Heaton John;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/GB00/02145 02/06/2000					
** FOREIGN APPLICATIONS VERIFIED: UNITED KINGDOM 9913713.5 06/14/1999 UNITED KINGDOM 9918097.8 08/03/1999					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 124-909	
Verified and Acknowledged Examiners's initials					
TITLE : Method of fabricating a semiconductor device					
<small>U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL		PREPARED FOR ISSUE	Application Examiner		